

L Number	Hits	Search Text	DB	Time stamp
-	527	complex adj programmable adj logic adj device\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:01
-	0	((complex adj programmable adj logic adj device\$1) same (dsp processor microprocessor cpu (digital adj signal adj controller)) same sdram same ((media adj access adj control) fifo) same network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:03
-	0	((complex adj programmable adj logic adj device\$1) same (dsp processor microprocessor cpu (digital adj signal adj controller)) same sdram same ((media adj access adj control) fifo) and network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:03
-	0	((complex adj programmable adj logic adj device\$1) same (dsp processor microprocessor cpu (digital adj signal adj controller)) same sdram same ((media adj access adj control) fifo)) and network	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:04
-	1	((complex adj programmable adj logic adj device\$1) same (dsp processor microprocessor cpu (digital adj signal adj controller)) same (memory dram sdram) same ((media adj access adj control) fifo)) and network.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:05
-	238	read same write same simultaneous\$2 same dma	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:07
-	0	((read same write same simultaneous\$2 same dma) and ((read adj3 (dsp procesor)) same (write adj3 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:08
-	453	(data adj recorder) and instrument	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:56
-	5392	data adj recorder	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:56
-	0	((data adj recorder) same (memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same simultaneous\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:57
-	360	(memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same simultaneous\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:59
-	314	((memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same simultaneous\$2) and @ad < 20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 14:59

-	68	(memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same simultaneous\$2 same read same write	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 15:01
-	54	((memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same simultaneous\$2 same read same write) and @ad < 20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 15:02
-	84	(memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same (concurrent\$2 simultaneous\$2) same read same write	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 15:02
-	68	((memory sdram) same (mca fifo) same (microprocessor processor cpu dsp) same (concurrent\$2 simultaneous\$2) same read same write) and @ad < 20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 16:23
-	342	(read same write) same simultaneously same different same device\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 16:23
-	232	((read same write) same simultaneously same different same device\$1) and @ad < 20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 16:31
-	30	((read same write) same simultaneously same different same device\$1) and @ad < 20000929) and (different adj3 device\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 16:32
-	2363	DMA.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 18:03
-	0	DMA.ti. and (complex adj programmable adj logic adj device\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 18:03
-	0	DMA.ti. and (((read same write) same simultaneously) same fifo same sdram same (cpu dsp processor microprocessor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 18:05
-	0	DMA.ti. and (((read same write) same simultaneously) same fifo same memory same (cpu dsp processor microprocessor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 18:05
-	0	DMA.ti. and (((read and write) same simultaneously) same fifo same sdram same (cpu dsp processor microprocessor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/04 18:06

-	939	media adj access adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 08:58
-	104	(media adj access adj controller) same fifo	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 08:52
-	213	((media adj access adj controller) same fifo) non-addressable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 08:52
-	0	((media adj access adj controller) same fifo) and non-addressable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 08:52
-	60	(media adj access adj controller).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 08:59
-	51	((media adj access adj controller).ti.) and @ad<20000929 and (media adj access adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 09:01
-	7	((media adj access adj controller).ti.) and @ad<20000929 and ((media adj access adj controller) same fifo)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 13:53
-	474	non-addressed	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 13:53
-	207	non-addressed same (device\$1 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 09:09
-	11	non-addressed same fifo	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 09:42
-	28	cache same miss same simultaneously same read same write same (ram memory sdram dram) same controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 09:44
-	27	(cache same miss same simultaneously same read same write same (ram memory sdram dram) same controller) and @ad<20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 10:37

-	0	((media adj access adj controller).ti.) and @ad<20000929 and ((media adj access adj controller) same fifo same non-addressed)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/05 14:16
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